Dr. Makoto Nagata is a professor and currently the dean of the graduate school of science, technology and innovation, Kobe University, Japan. His research interests include design techniques targeting high-performance mixed analog, RF and digital VLSI systems with particular emphasis on power/signal/substrate integrity and electromagnetic compatibility, testing and diagnosis, three-dimensional system integration, as well as their applications for hardware security and safety. Dr. Nagata chaired the Technology Directions subcommittee for International Solid-State Circuits Conference (2018-2022). He was a technical program chair, a symposium chair and an executive committee member for the Symposium on VLSI circuits (2010-2014). He was an AdCom member to the IEEE SSCS (2020-2022) and also serves as a distinguished lecturer (DL) in the society (2020-2021). He is an associate editor for IEEE Transactions on VLSI Systems. He is a Senior Member of IEEE.