Dr. **Gang Qu** is a professor in the Department of Electrical and Computer Engineering at the University of Maryland, College Park. He has worked extensively in the area of hardware security with more than 250 publications and delivered more than 120 invited talks. He has also contributed in building the hardware security and trust community. Notably, he was an individual member of the VSIA intellectual property protection (IPP) working group (2001) and contributed to VSIA's IPP standards; he published the first book (2003) on hardware security, Intellectual Property Protection in VLSI Designs: Theory and Practice, based on his Ph.D. dissertation (2000); he developed a MOOC of hardware security on Coursera (2014) that has attracted tens of thousands of students all over the world; he co-founded the AsianHOST symposium (2016) and the IEEE hardware security and trust technique committee (HSTTC, 2020). He has been involved in HOST from its first edition (2010) and served as the TPC chair (2018) and general chair (2019). He led a tutorial in HOST 2020 entitled "Security Issues in AI and Their Impacts on Hardware Security". He is currently leading the IV&V team on side channel analysis in the DARPA AISS project. Since October 2021, he has been working in the NSF SaTC program. He is a fellow of IEEE.