



Qiaoyan Yu's research expertise includes hardware Trojan detection, side-channel attack mitigation, 3DIC and embedded system security, cybersecurity, and VLSI fault tolerance.

Qiaoyan received her B.S. from Xidian University (2002), M.S. in Communication and Information Engineering from Zhejiang University (2005), and the Ph.D. in Electrical and Computer Engineering from the University of Rochester (2011). She joined the University of New Hampshire as Assistant Professor in 2011 and started to direct the Reliable &

Secure VLSI Systems Laboratory since then. She was a recipient of the NSF CAREER Award and VFRP fellowship at Air Force Research Laboratory. She serves on the technical program committees of HOST, Asian HOST, DAC, ASP-DAC, GLSVLSI, ISVLSI, DFT, ISCAS, and ICCD. Qiaoyan Yu presented a tutorial talk at GLSVLSI'17.