Patrick Schaumont's research interests are in design and design methods of secure, efficient and real-time embedded computing systems. He received the Ph.D. degree in Electrical Engineering from UCLA in 2004 and the MS degree in Computer Science from Ghent University in 1990. From 1992 to 2000 he was a staff researcher at IMEC, Belgium. From 2001 until 2005 he was a student at UCLA. He joined Virginia Tech in 2005. From 2012 to 2014 he served as Director of the Center for Embedded Systems for Critical Applications (CESCA) at Virginia Tech. In 2014 he was a visiting researcher at the National Institute of Information and Telecommunications Technology (NICT) in Japan. He served as program co-chair for several conferences in cryptographic and secure engineering, including HOST in 2011, CHES in 2012, and FDTC in 2017. Patrick Schaumont presented several tutorials and short courses on topics in Secure Embedded Systems Design. This includes talks at UCSB in 2013, at ASP-DAC in 2015, at Fudan University in 2016, and at IIT Kanpur in 2016.