HOST 2018: IEEE International Symposium on Hardware-Oriented Security and Trust
May 2018, Washington DC Metro Area

HOST 2018
http://hostsymposium.org

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Best Paper Awards: Top papers will be considered for best paper awards. For best student paper award, the paper's first author and presenter must be a full-time student.

Grants: Travel grants will be available for graduate and undergraduate students.

CALL FOR PAPERS

IEEE International Symposium on Hardware Oriented Security and Trust (HOST) aims to facilitate the rapid growth of hardware-based security research and development. HOST highlights new results in the area of hardware security. Relevant research topics include architectures, design methods, circuits, and applications of secure hardware. HOST 2018 invites original contributions related to (but not limited to) the following:

- **Hardware security primitives (Crypto, PUFs, RNGs)**
- **Hardware design techniques to facilitate software and/or system security**
- **Architecture support for security**
- **Side-channel analysis, attacks, and protection**
- **Hardware Trojan attacks, detection, and countermeasures**
- **Hardware security test and verification**
- **IoT, automotive, and cyber-physical system security**
- **FPGA and system-on-chip (SoC) security**
- **Supply chain risk mitigation (e.g., counterfeit detection & avoidance)**
- **Reverse engineering and hardware obfuscation**
- **Fault injection and mitigation**
- **Metrics, policies, assessment, and standards related to hardware security**
- **Hardware IP trust (watermarking, metering, trust verification)**
- **Trusted manufacturing including split manufacturing and 2.5/3D integration**
- **Hardware tampering attacks and protection**
- **Design and applications of emerging and nanoscale devices for security**
- **Sensor-enabled hardware security**
- **Machine learning for hardware security**
- **Hardware acceleration for security applications**
- **Applications of cyber deception in hardware security**
- **Privacy-preserving computing, secure function evaluation**
- **Architecture and hardware-enabled SoC, cyber, data center, etc. security**
- **Hardware-assisted system security (this might be a different way of saying one of the previously mentioned)**

You can register and submit your paper at https://easychair.org/conferences/?conf=host2018. The page limit is **8 pages**, double column, IEEE format, with a minimum font size of 10 point. Authors will be asked if they want their paper to be considered for a **4-page poster paper** (if it is not accepted as a full paper). Note that poster papers will be considered as publications. The paper selection will involve a **double blind review process**. Therefore, the identity of authors must not be revealed, directly or indirectly, anywhere in the manuscript. Only work that has not been previously published at the time of the conference will be considered. **Duplicate submissions** to concurrent conferences are not permissible, and if encountered will be rejected and reported to IEEE.

HOST 2018 will accept **proposals for workshops, tutorials, and demos** on Easychair after the paper submission deadline. More information will be disseminated in future Calls.

IMPORTANT DATES:

Registration of Title + Abstract:	Oct. 9, 2017
Paper Submission (Notification):	Oct. 23, 2017 (Jan. 15, 2017)
Workshop Proposal Submission (Notification):	Dec. 15, 2017 (Jan. 30, 2018)
Tutorial Proposal Submission (Notification):	Dec. 15, 2017 (Jan. 15, 2018)
HW Demo Proposal Submission (Notification):	Jan. 15, 2018 (Feb. 15, 2018)
Camera-ready Paper Submission:	Feb. 23, 2018

Technical Program: Gang Qu
Tel: +1 (301) 405-6703
E-mail: gangqu@umd.edu

General Information: Ryan Kastner
Tel: +1 (858) 534-8908
E-mail: kastner@ucsd.edu

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