

**Prof. Saqib** is currently working at Florida Institute of Technology where she is an assistant professor of Electrical and Computer Engineering. Her research focus is on hardware security and trust, design for

manufacturability and high performance computing for big data. Dr. Saqib is Principal Investigator (PI) of NSF funded projects on HAT: Hardware based Authentication and Trusted- Platform-Module Functions for IoTs and for HACE Lab: An On-Line Hardware Security Attack and Countermeasure Evaluation in collaboration with University of Florida. Professor Fareena Saqib serves as program committee on HOST, ASP-DAC, and ETS. Dr. Saqib has served/is serving as panelist for National Science Foundation (NSF) Grants, and Florida Center for Cybersecurity (FCC)'s collaborative seed grant program. Further, Dr. Fareena Saqib is also serving as a technical reviewer for various professional journals and conference publications such as Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD), 2015-2016; International Symposium on Hardware-Oriented Security and Trust (HOST), 2015-2016; Transactions on Emerging Topics in Computing (TETC), 2015-2016; Transaction on Information Forensic Security (TIFS), 2015; Transactions on Circuits and Systems (TCAS), 2015; Transactions on Computers (TC), 2015; IEEE Symposium on Circuits and Systems (ISCAS), 2016; Journal of Electronics Testing, Theory and Practice (JETTA), 2016; Journal IEEE Computer (COMSI), 2016.