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University of New Mexico. His research interests are in the area of nano-scale VLSI and include security and trust in IC hardware, silicon validation, design for manufacturability and delay test methods. Dr. Plusquellic received an "Outstanding Contribution Award" from IEEE Computer Society in 2012 for co-founding and for his contributions to the Symposium on Hardware-Oriented Security and Trust (HOST). He served as General Chair for HOST in 2010, for the Defect-Based Testing Workshop in 2006 and as an Associate Editor for Transactions on Computers. He is currently Hardware Demonstration for HOST. He received the "10 Years of Continuous Service Award" from the International Test Conference, a Best Paper Award from VTS, an ACM Distinguished Service Award from SIGDA and two Austin CAS Fellow Awards from IBM. He recently received a "2014 Innovation Award" from the Science and Technology Center at the University of New Mexico, is a "Featured Entrepreneur" within the School of Engineering and has 3 patents and several provisional applications filed with the US. Patent and Trademark Office. Professor Plusquellic is serving on the Program Committees for HOST and GLSVLSI. He has published more than 70 refereed conference and journal papers. He is a Golden Core Member of the IEEE Computer Society and a member of the IEEE.