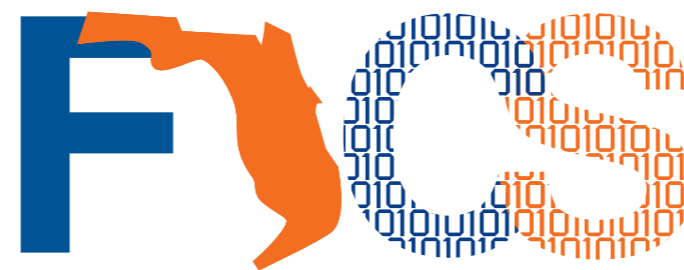# Hardware IP Protection Through Invasive and Non-Invasive Analysis

Moderator:
Mark M. Tehranipoor

Questions

- What is the value in chip level reverse engineering? What is the state of of knowledge? What are the challenges as technology further scales down to 10nm or 7nm?

- What are the challenges in dealing with IP protection?

- What challenges IoTs bring to protection at all levels?

- What threat models makes sense in this complex supply chain?

- What lessons to learn from software community about IP protection?

- What applications worry you the most that needs reverse engineering and IP protection?

Panelists

- **Ed Principe, TESCAN**

- **Chris Pawlowicz, Techinsights**

- **Orlando Pompei, AFRL**

- **Steve Trimberger, Xilinx**

- **Yier Jin, University of Central Florida**

- **Matthew Scholl, NIST**